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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> ( Not for submission under 37 CFR 1.99)	Application Number		10550323
	Filing Date		2005-09-23
	First Named Inventor	Roger D. Chamberlain	
	Art Unit	<del>2484</del> 2162	
	Examiner Name	<del>Heming, Matthew T.</del> JBF	
	Attorney Docket Number	53047-57370	

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	3	7386564		2008-06-10	Abdo et al.	
	4	7408932		2008-08-05	Kounavis et al.	
	5	7411957		2008-08-12	Stacy et al.	
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	1	THOMPSON et al., "The CLUSTAL_X Windows Interface: Flexible Strategies for Multiple Sequence Alignment Aided by Quality Analysis Tools", Nucleic Acids Research, 1997, Vol. 25, No. 24, pp. 4876-4882	<input type="checkbox"/>
	2	WARD ET AL., "Dynamically Reconfigurable Computing: A Novel Computation Technology with Potential to Improve National Security Capabilities", May 15, 2003, A White Paper Prepared by Star Bridge Systems, Inc. [retrieved 2006-12-12]. Retrieved from the Internet: <URL: <a href="http://www.starbridgesystems.com/resources/whitepapers/Dynamically%20Reconfigurable%20Computing.pdf">http://www.starbridgesystems.com/resources/whitepapers/Dynamically%20Reconfigurable%20Computing.pdf</a> .All Pages.	<input type="checkbox"/>
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	Attorney Docket Number	53047-57370	

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5	Written Opinion for PCT/US2004/016398 dated June 9, 2005 . <b>All Pages</b> .	<input type="checkbox"/>
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7	ZIV ET AL., "Compression of Individual Sequence via Variable-Rate Coding", IEEE Transactions on Information Theory, Sept. 1978, pp. 530-536, Vol. IT-24, No. 5, Institute of Electrical and Electronics Engineers, Washington, DC, USA	<input type="checkbox"/>

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